













Datasheet

KOE

TX18D200VM0EAA

7.0" TFT

KO-01-008

The information contained in this document has been carefully researched and is, to the best of our knowledge, accurate. However, we assume no liability for any product failures or damages, immediate or consequential, resulting from the use of the information provided herein. Our products are not intended for use in systems in which failures of product could result in personal injury. All trademarks mentioned herein are property of their respective owners. All specifications are subject to change without notice.



Kaohsiung Opto-Electronics Inc.

FOR MESSRS : _____

DATE : Nov. 8th ,2013

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

TX18D200VM0EAA

Contents

No.	ITEM	SHEET No.	PAGE
1	COVER	7B64PS 2701-TX18D200VM0EAA-1	1-1/1
2	RECORD OF REVISION	7B64PS 2702-TX18D200VM0EAA-1	2-1/1
3	GENERAL DATA	7B64PS 2703-TX18D200VM0EAA-1	3-1/1
4	ABSOLUTE MAXIMUM RATINGS	7B64PS 2704-TX18D200VM0EAA-1	4-1/1
5	ELECTRICAL CHARACTERISTICS	7B64PS 2705-TX18D200VM0EAA-1	5-1/2~2/2
6	OPTICAL CHARACTERISTICS	7B64PS 2706-TX18D200VM0EAA-1	6-1/2~2/2
7	BLOCK DIAGRAME	7B64PS 2707-TX18D200VM0EAA-1	7-1/1
8	RELIABILITY TESTS	7B64PS 2708-TX18D200VM0EAA-1	8-1/1
9	LCD INTERFACE	7B64PS 2709-TX18D200VM0EAA-1	9-1/7~7/7
10	OUTLINE DIMENSIONS	7B64PS 2710-TX18D200VM0EAA-1	10-1/2~2/2
11	APPEARANCE STANDARD	7B64PS 2711-TX18D200VM0EAA-1	11-1/3~3/3
12	PRECAUTIONS	7B64PS 2712-TX18D200VM0EAA-1	12-1/2~2/2
13	DESIGNATION OF LOT MARK	7B64PS 2713-TX18D200VM0EAA-1	13-1/1

PROPOSED BY: Centher

PAGE 1-1/1

2. REC	ORD OF REVISIC	N			
DATE	SHEET No.		SUMMARY		
L	<u> </u>				
KAOHSIUNG	OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2702-TX18D200VM0EAA-1	PAGE	2-1/1
		NU.			

3. GENERAL DATA

3.1 DISPLAY FEATURES

This module is a 7" FHD of 16:9 format amorphous silicon TFT. The pixel format is vertical stripe and sub pixels are arranged as R (red), G (green), B (blue) sequentially. This display is RoHS compliant, COG (chip on glass) technology and LED backlight are applied on this display.

Part Name	TX18D200VM0EAA
Module Dimensions	169.0(W) mm x 104.0(H) mm x 10.0 (D) mm typ.
LCD Active Area	155.52(W) mm x 87.48(H) mm
Pixel Pitch	0.081(W) mm x 0.081 (H) mm
Resolution	1920 x 3(RGB)(W) x 1080(H) dots
Color Pixel Arrangement	R, G, B Vertical Stripe
LCD Type	Transmissive Color TFT; Normally Black
Display Type	Active Matrix
Number of Colors	16.7M Colors
Backlight	3 LEDs Series x 10 Parallel (30 LEDs in Total)
Weight	180 typ. (g)
Interface	LVDS; 20 pins
Power Supply Voltage	3.3V for LCD; 12V for Backlight
Power Consumption	0.53W for LCD; 4.62W for Backlight
Viewing Direction	Super Wide Version (In-Plane Switching)

4. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remarks
Supply Voltage	V _{DD}	-0.3	4.0	V	-
Input Voltage of Logic	VI	-0.3	V _{DD} +0.3	V	Note 1
Operating Temperature	Тор	-20	70	°C	Note 2
Storage Temperature	Tst	-30	80	°C	Note 2
Backlight Input Voltage	V_{LED}	-	15	V	-

Note 1: The rating is defined for the signal voltages of the interface such as CLK and pixel data pairs.

- Note 2: The maximum rating is defined as above based on the chamber temperature, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:
 - Background color, contrast and response time would be different in temperatures other than $25\,^\circ\mathrm{C}\,.$
 - Operating under high temperature will shorten LED lifetime.

5. ELECTRICAL CHARACTERISTICS

5.1 LCD CHARACTERISTICS

5.1 LCD CHARACTERISTICS $T_a = 25 \ ^{\circ}C, \ Vss = 0$							$V_{ss} = 0V$
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	V _{DD}	-	3.0	3.3	3.6	V	-
Differential Input	N	"H" level	-	-	+100		Nata 4
Voltage for LVDS Receiver Threshold	VI	"L" level	-100	-	-	mV	Note 1
Power Supply Current	I _{DD}	$V_{DD}=3.3V$	-	160	210	mA	Note 2
Vsync Frequency	f_v	-	-	60	67	Hz	
Hsync Frequency	$f_{\scriptscriptstyle H}$	-	66	67.5	69	KHz	Note 3
CLK Frequency	$f_{\textit{CLK}}$	-	135.3	148.5	160	MHz	

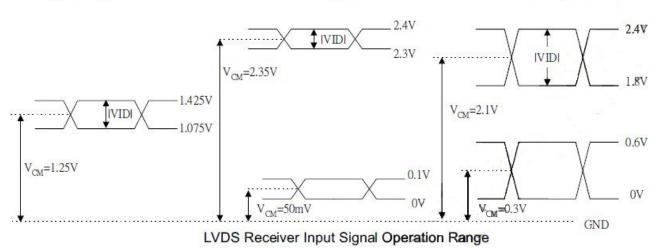
Note 1: VCM=+1.2V

VCM is common mode voltage of LVDS transmitter/receiver.

Typical Input Swim

Minimum Input Swim

Maximum Input Swim

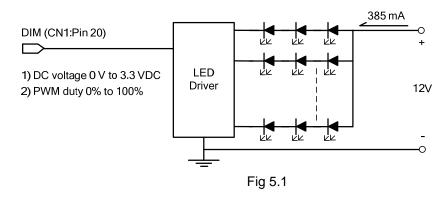


Note 2: An all white check pattern is used when measuring I_{DD} . f_{y} is set to 60 Hz.

- Note 3: For LVDS transmitter input.
- Note 4: 1.0A fuse is applied in the module for IDD. For display activation and protection purpose, power supply is recommended larger than 2.5A to start the display and break fuse once any short circuit occurred.

5.2 BACKLIGHT CHARACTERISTICS $T_a = 25 \ ^{\circ}C$							
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks
LED Input Voltage	V_{LED}	-	11.7	12	12.3	V	Note1
LED Forward Current (Dim Control)		0V; 0% duty	-	385	-	mA	Note 2
		3.3VDC; 100% duty	-	65	-		
LED lifetime	-	I _{LED} = 385 mA	-	70K	-	hrs	Note 3

- Note 1: As Fig. 5.1 shown, LED current is constant, 385 mA, controlled by the LED driver when applying 12V.
- Note 2: Dimming function can be obtained by applying DC voltage or PWM signal from the display interface CN1. The recommended PWM signal is 1K ~ 10K Hz with 3.3V amplitude.
- Note 3: The estimated lifetime is specified as the time to reduce 50% brightness by applying 430 mA at $25\,^\circ\mathrm{C}\,.$



KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2705-TX18D200VM0EAA-1	PAGE	5-2/2
---------------------------------	--------------	------------------------------	------	-------

6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The backlight unit needs to be turned on for 30 minutes.
- The ambient temperature is 25 $^{\circ}\mathrm{C}\,.$
- In the dark room around 500~1000 lx, the equipment has been set for the measurements as shown in Fig 6.1.

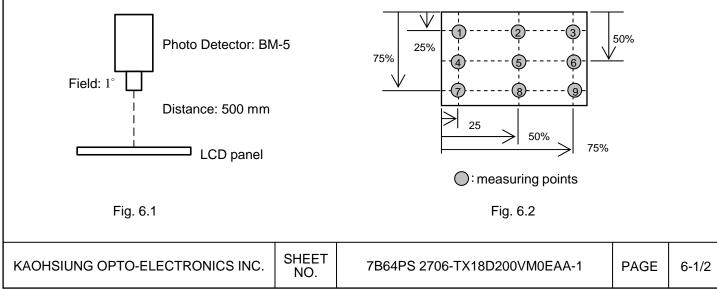
~ ~ *`* ′

r			Γ	1	l _a	= 25 °C,	$f_v = 60 \text{Hz},$	VDD = 3.3V
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks
Brightness o	of White	-		560	700	-	cd/m ²	Note 1
Brightness U	niformity	-	$\phi = 0^{\circ}, \theta = 0^{\circ},$	70	-	-	%	Note 2,3
Contrast F	Ratio	CR	I _{LED} = 385 mA	500	800	-	-	Note 4
Response (Rising + Fa		$T_r + T_f$	$\phi = 0^\circ, \theta = 0^\circ$	-	23	-	ms	Note 5
		$\theta \mathbf{x}$	$\phi = 0^{\circ}, CR \ge 10$	-	85	-		
Viewing A	nala	$\theta \mathbf{x}'$	φ = 180°, CR ≥ 10	-	85	-	D	Nata C
viewing A	Viewing Angle		$\phi = 90^{\circ}, CR \ge 10$	-	85	-	Degree	Note 6
		θ y'	$\phi = 270^{\circ}, CR \ge 10$	-	85	-		
	Ded	Х	_	0.60	0.65	0.70		
	Red	Y		0.27	0.32	0.37		
	Graan	Х		0.28	0.33	0.38		
Color	Green	Y		0.57	0.62	0.67		
Chromaticity	Plue	Х	$\phi = 0^\circ, \theta = 0^\circ$	0.10	0.15	0.20		Note 7
	Blue	Y		0.01	0.06	0.11		
	White	Х		0.27	0.32	0.37		
	vville	Y]	0.29	0.34	0.39	1	

Note 1: The brightness is measured from the panel center point, P5 in Fig. 6.2, for the typical value. Note 2: The brightness uniformity is calculated by the equation as below:

 $Brightness \, uniformity = \frac{Min.\,Brightness}{Max.\,Brightness} \times 100\%$

, which is based on the brightness values of the 9 points measured by BM-5 as shown in Fig. 6.2.



Note 3: Continuously operating the test pattern (see below chess pattern Fig.6.3) on display for 2 hours at 25° C then switch to completely white pattern, the previous test pattern shall disappear within 2 seconds.

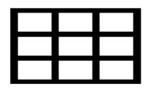
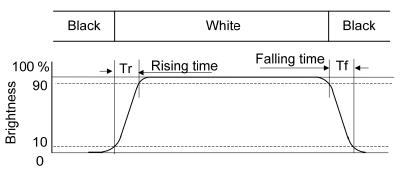


Fig.6.3

Note 4: The Contrast Ratio is measured from the center point of the panel, P5, and defined as the following equation:

 $CR = \frac{Brightness of White}{Brightness of Black}$

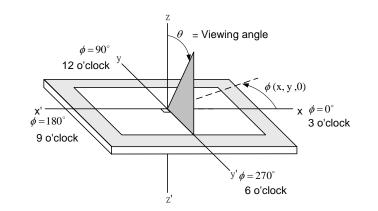
Note 5: The definition of response time is shown in Fig. 6.4. The rising time is the period from 10% brightness to 90% brightness when the data is from black to white. Oppositely, Falling time is the period from 90% brightness falling to 10% brightness.





Note 6: The definition of viewing angle is shown in Fig. 6.5. Angle ϕ is used to represent viewing directions, for instance, $\phi = 270^{\circ}$ means 6 o'clock, and $\phi = 0^{\circ}$ means 3 o'clock. Moreover, angle θ is used to represent viewing angles from axis Z toward plane XY.

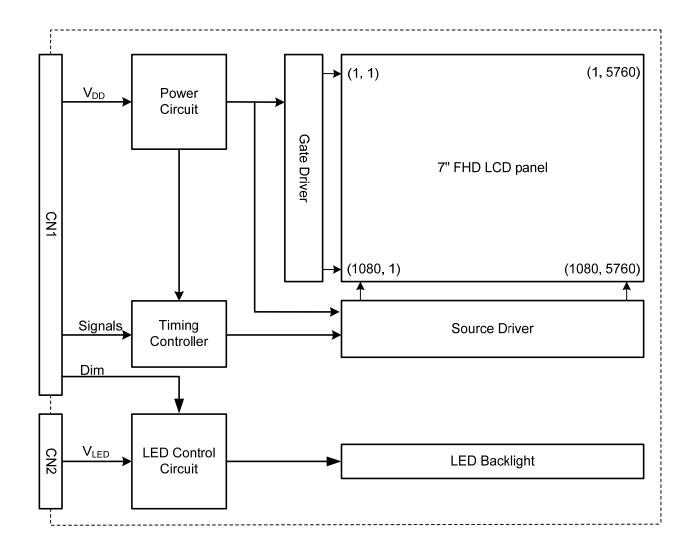
The display is super wide viewing angle version, so that the best optical performance can be obtained from every viewing direction.





Note 7: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig. 6.2.

7. BLOCK DIAGRAM



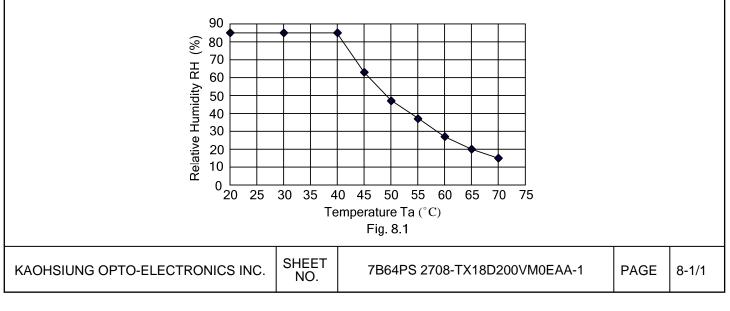
Note 1: Signals are CLK and pixel data pairs.

8. RELIABILITY TESTS

Test Item	Condition	· · · · · · · · · · · · · · · · · · ·
High Temperature	1) Operating 2) 70 ° _C	240 hrs
Low Temperature	1) Operating 2) -20 °C	240 hrs
High Temperature	1) Storage 2) 80 °C	240 hrs
Low Temperature	1) Storage 2) -30 °C	240 hrs
Heat Cycle	1) Operating 2) –20°C ~70°C 3) 3hrs~1hr~3hrs	240 hrs
Thermal Shock	 Non-Operating -35 °C ↔ 85 °C 0.5 hr ↔ 0.5 hr 	240 hrs
High Temperature & Humidity	 1) Operating 2) 40 °C & 85%RH 3) Without condensation (Note4) 	240 hrs
Vibration	 Non-Operating 2) 20~200 Hz 3) 2G 4) X, Y, and Z directions 	1 hr for each direction
Mechanical Shock	 1) Non-Operating 2) 10 ms 3) 50G 4) ±X, ±Y and ±Z directions 	Once for each direction
ESD	 Operating Tip: 200 pF, 250 Ω Air discharge for glass: ± 8KV Contact discharge for metal frame: ± 8KV 	1) Glass: 9 points 2) Metal frame: 8 points (Note3)

Note 1: Display functionalities are inspected under the conditions defined in the specification after the reliability tests.

- Note 2: The display is not guaranteed for use in corrosive gas environments.
- Note 3: All pins of LCD interface (CN1) have been tested by $\pm 100V$ contact discharge of ESD under non-operating condition.
- Note 4: Under the condition of high temperature & humidity, if the temperature is higher than 40°C, the humidity needs to be reduced as Fig. 8.1 shown.



9. LCD INTERFACE

9.1 INTERFACE PIN CONNECTIONS

The display interface connector (CN1) is FI-SEB20P-HF13E-E1500 made by JAE and pin assignment is as below:

Pin No.	Symbol	Signal	Pin No.	Symbol	Signal
1	V_{DD}	Device Cumply for Logic	11	IN2-	
2	V _{DD}	Power Supply for Logic	12	IN2+	B2~B5, DE
3	V _{SS}		13	V _{SS}	GND
4	V _{SS}	GND	14	CLK IN-	Divel Cleak
5	IN0-	R0~R5, G0	15	CLK IN+	Pixel Clock
6	IN0+	- KU~KS, GU	16	V _{SS}	GND
7	V _{SS}	GND	17	IN3-	
8	IN1-		18	IN3+	R6~R7, G6~G7, B6~B7
9	IN1+	- G1~G5, B0~B1	19	NC	No Connection
10	V _{SS}	GND	20	DIM	Note 2

Note 1: IN n- and IN n+ (n=0, 1, 2, 3), CLK IN- and CLK IN+ should be wired by twist-pairs or side-by-side FPC patterns, respectively.

Note 2: Normal brightness: 0V or 0% PWM duty; Brightness control: 0V to 3.3V DC or 0% to 100% PWM duty.

The backlight connector (CN2) is SM02(8.0)B-BHS-1-TB(LF)(SN) made by JST, and pin assignment is as below:

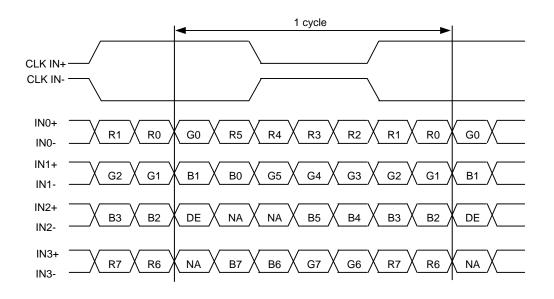
Pin No.	Signal	Signal
1	V _{LED}	12VDC
2	GND	Ground

9.2 LVDS INTERFACE

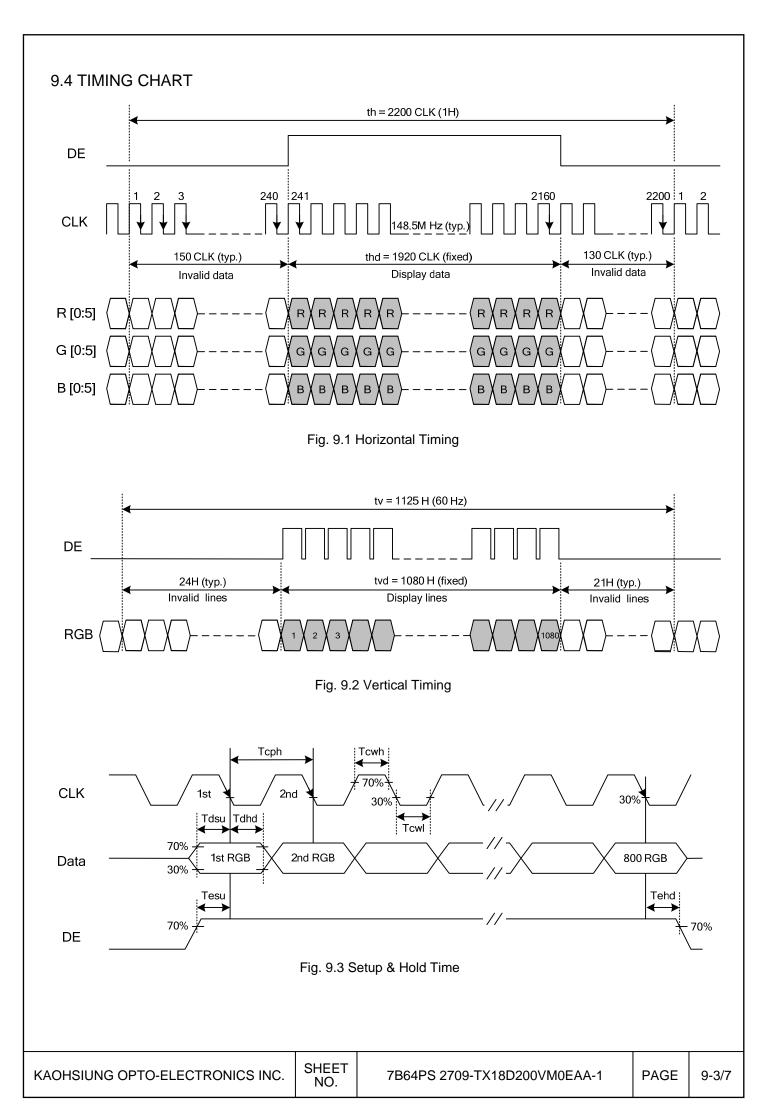
Machine Side	9	CN1 _ (interface)	TFT-LCD Side	
Controll R0-R5,G0 G1-G5,B0,B1 7 TB0-6 7 TC0-6 R6,R7,G6,G7,B6, B7,NA CK CLK IN	2) THC63LVDM87	1) IN0+ IN0- IN1+ IN1- IN2+ IN3- CLK IN+ CLK IN+	Controller With Multi-I/F Receiver and Transmitter	

- Note 1: LVDS cable impedance should be 100 ohms per signal line when each 2-lines (+, -) is used in differential mode.
- Note 2: The recommended transmitter, THC63LVDM87, is made by Thine or equivalent, which is not contained in the module.

9.3 LVDS DATA FORMAT



DE: Display Enable NA: Not Available



9.5 TIMING TABLE

The column of timing sets including minimum, typical, and maximum as below are based on the best optical performance, frame frequency (Vsync) = 60Hz to define.

A. DE MODE

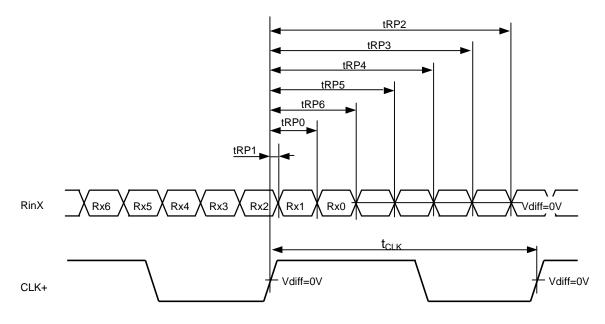
	Item	Symbol	Min.	Тур.	Max.	Unit		
	CLK Frequency	fclk	135.3	148.5	160	MHz		
Horizontal	ontal Display Data			1920				
	Cycle Time	th	2050	2200	2320	CLK		
Martinal	Display Line	tvd		1080				
Vertical	Cycle Time	tv	1100	1125	1150	Н		

B. CLOCK AND DATA INPUT TIMING

	ltem	Symbol	Min.	Тур.	Max.	Unit
01.14	Duty	Tcwh	47.5	50	52.5	%
CLK	Cycle Time	Tcph	-	6.74	-	
Data	Setup Time	Tdsu	1	-	-	
Data	Hold Time	Tdhd	1	-	-	ns
	Setup Time		1	-	-	
DE	Hold Time	Tehd	1	-	-	

KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2709-TX18D200VM0EAA-1	PAGE	9-4/7
---------------------------------	--------------	------------------------------	------	-------

9.6 LVDS RECEIVER TIMING



RinX= (RinX+)-(RinX-) (X=0, 1, 2, 3)

	Item	Symbol	Min.	Тур.	Max.	Unit
CLK	Cycle frequency	1/tcLK	135.3	148.5	160	MHz
	0 data position	tRP0	1/7* t _{CLK} -0.49	1/7* t _{CLK}	1/7* t _{CLK} +0.49	
	1st data position	tRP1	-0.49	0	+0.49	
DieV	2nd data position	tRP2	6/7* t _{CLK} -0.49	6/7* t _{CLK}	6/7* t _{CLK} +0.49	
RinX	3rd data position	tRP3	5/7* t _{CLK} -0.49	5/7* t _{CLK}	5/7* t _{CLK} +0.49	ns
(X=0,1,2,3)	4th data position	tRP4	4/7* t _{CLK} -0.49	4/7* t _{CLK}	4/7* t _{CLK} +0.49	
	5th data position	tRP5	3/7* t _{CLK} -0.49	3/7* t _{CLK}	3/7* t _{CLK} +0.49	
	6th data position	tRP6	2/7* t _{CLK} -0.49	2/7* t _{CLK}	2/7* t _{CLK} +0.49	

9.7 POWER SEQUENCE

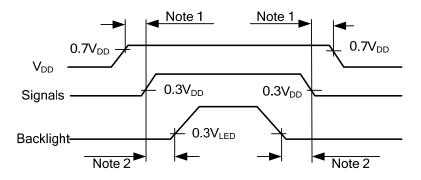


Fig. 9.4 Power Sequence Timing

- Note 1: In order to avoid any damages, V_{DD} has to be applied before all other signals. The opposite is true for power off where V_{DD} has to be remained on until all other signals have been switch off. The recommended time period is 1 second.
- Note 2: In order to avoid showing uncompleted patterns in transient state. It is recommended that switching the backlight on is delayed for 1 second after the signals have been applied. The opposite is true for power off where the backlight has to be switched off 1 second before the signals are removed.

KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2709-TX18D200VM0EAA-1	PAGE	9-6/7
---------------------------------	--------------	------------------------------	------	-------

9.8 DATA INPUT for DISPLAY COLOR

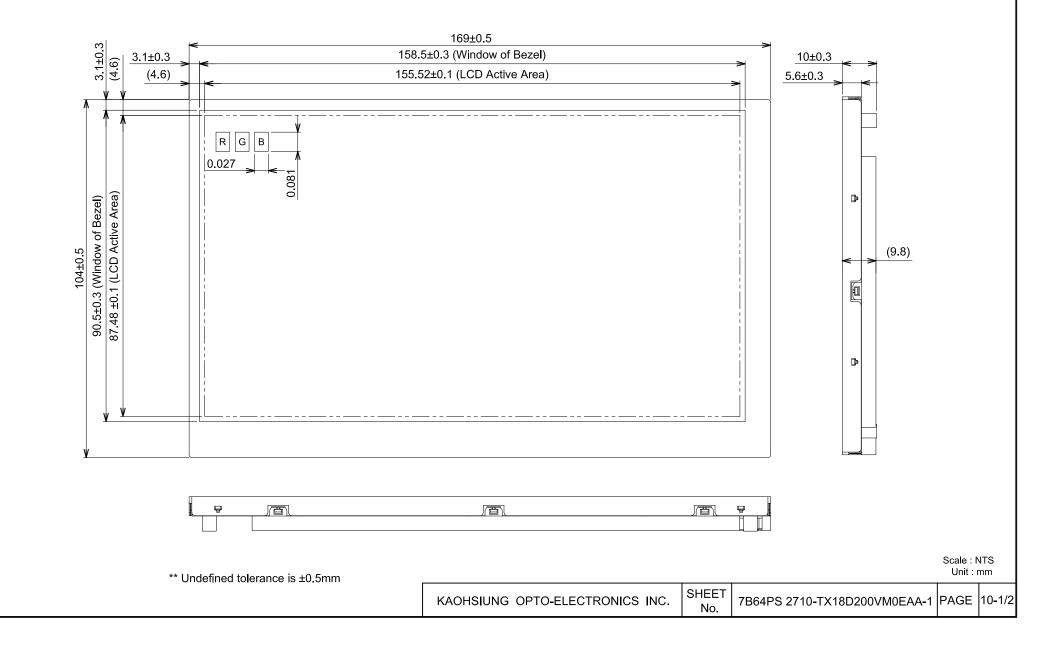
				1	Red	Data	à	0				G	Greer	Dat	a						Blue	Data	l		
Inp	ut color	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	В
		MSB							LSB	MSB							LSB	MSB							LS
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	(
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	(
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	(
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	,
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	(
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	(
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Red	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	(
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	(
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	(
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	(
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	(
Green	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	(
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	(
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	(
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	(
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	(
Blue	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	

Note 1: Definition of gray scale : Color(n) Number in parenthesis indicates gray scale level. Larger number corresponds to brighter level.

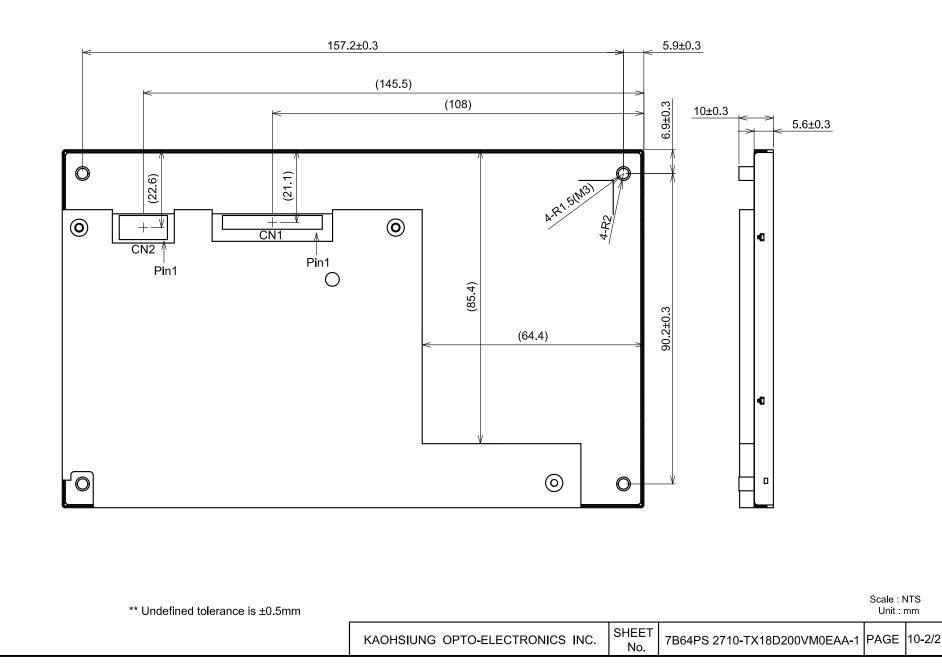
SHEET NO.

Note 2: Data Signal : 1 : High, 0 : Low

10. OUTLINE DIMENSIONS 10.1 FRONT VIEW



10.2 REAR VIEW



11. APPEARANCE STANDARD

The appearance inspection is performed in a dark room around 500~1000 lx based on the conditions as below:

- The distance between inspector's eyes and display is 30 cm.
- The viewing zone is defined with angle θ shown in Fig. 11. The inspection should be performed within 45° when display is shut down. The inspection should be performed within 5° when display is power on.

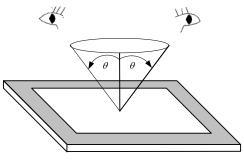


Fig. 11.1

11.1 THE DEFINITION OF LCD ZONE

LCD panel is divided into 3 areas as shown in Fig.11.2 for appearance specification in next section. A zone is the LCD active area (dot area); B zone is the area, which extended 1 mm out from LCD active area; C zone is the area between B zone and metal frame.

In terms of housing design, B zone is the recommended window area customers' housing should be located in.

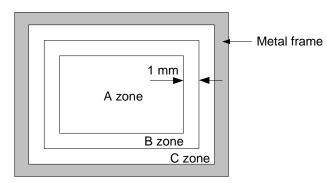


Fig. 11.2

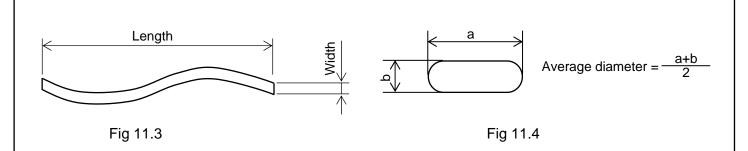
KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2711-TX18D200VM0EAA-1	PAGE	11-1/3

11.2 LCD APPEARANCE SPECIFICATION

The specification as below is defined as the amount of unexpected phenomenon or material in different zones of LCD panel. The definitions of length, width and average diameter using in the table are shown in Fig. 11.3 and Fig. 11.4.

Item			Cri	teria			Applied zone	
	Length (mm) Wid		n (mm)	Maximum nu	Imber	Minimum space		
	Ignored W		/≦0.01 Igno			-		
	L≦40	W≦	≦0.02	10		-		
O and take a	L≦20	W≦	≦0.04	10		-		
Scratches		A ∖ B						
	Average diameter (n	nm)	Maxim	um number	Mir	nimum space		
	D≦0.2		l	gnore		-		
	D≦0.4			10		-		
Dent		Se	erious one	is not allowed			А	
Wrinkles in polarizer		Se	erious one	is not allowed			А	
	Average diamet	ter (m	nm)	Max	imum n	umber		
Bubbles on polarizer	D≦0.3				Ignore	d	А	
Bubbles on polarizer	0.3 <d≦0< td=""><td>).5</td><td></td><td></td><td>10</td><td></td><td>~</td></d≦0<>).5			10		~	
	0.5 <d≦′< td=""><td>1.0</td><td></td><td></td><td>5</td><td></td><td></td></d≦′<>	1.0			5			
	Filamentous (Line shape)							
	Length (mm)		Width (mm)			imum number		
1) Stains 2) Foreign Materials	Ignored		W≦	≦0.02	Ignored		Α、Β	
	L≦2.0			≦0.03		10		
	L≦1.0		W≦	≦0.06		10		
				Dot shape)				
3) Dark Spot	Average diameter (mr	n)	Maximu	m number	Min	imum Space		
o) Dank oper	D≦0.3		Ign	ored		-		
	0.3 <d≦0.5< td=""><td></td><td></td><td>5 0</td><td></td><td>-</td><td>Α、Β</td></d≦0.5<>			5 0		-	Α、Β	
	D>0.5							
	In total							
	Т	hose	•	asily are accepta				
				ype	Max	imum number		
			1 dot		4			
			2 adjacent dot		1			
	Bright dot-defect	3		dot or above				
				nsity		2(¢ 20mm)		
Dot-Defect				total		5	А	
(Note 1)				dot		5		
		-		cent dot	<u> </u>	2		
	Dark dot-defect	3	-	dot or above		lot allowed		
				nsity	3	3(¢ 20mm)		
				total				
		in tota	total 10					

SHEET NO.



Note 1: The definitions of dot defect are as below:

- The defect area of the dot must be bigger than half of a dot.
- For bright dot-defect, showing black pattern, the dot's brightness must be over 30% brighter than others.
- For dark dot-defect, showing white pattern, the dot's brightness must be under 70% darker than others.
- The definition of 1-dot-defect is the defect-dot, which is isolated and no adjacent defect-dot.
- The definition of adjacent dot is shown as Fig. 11.5.
- The Density of dot defect is defined in the area within diameter ϕ =20mm.

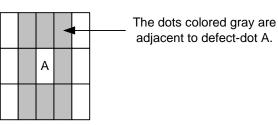


Fig. 11.5

KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2711-TX18D200VM0EAA-1	PAGE	11-3/3
---------------------------------	--------------	------------------------------	------	--------

12. PRECAUTIONS

12.1 PRECAUTIONS of ESD

- 1) Before handling the display, please ensure your body has been connected to ground to avoid any damages by ESD. Also, do not touch display's interface directly when assembling.
- 2) Please remove the protection film very slowly before turning on the display to avoid generating ESD.

12.2 PRECAUTIONS of HANDLING

- 1) In order to keep the appearance of display in good condition; please do not rub any surfaces of the displays by sharp tools harder than 3H, especially touch panel, metal frame and polarizer.
- 2) Please do not pile the displays in order to avoid any scars leaving on the display. In order to avoid any injuries, please pay more attention for the edges of glasses and metal frame, and wear finger cots to protect yourself and the display before working on it.
- 3) Touching the display area or the terminal pins with bare hand is prohibited. This is because it will stain the display area and cause poor insulation between terminal pins, and might affect display's electrical characteristics furthermore.
- 4) Do not use any harmful chemicals such as acetone, toluene, and isopropyl alcohol to clean display's surfaces.
- 5) Please use soft cloth or absorbent cotton with ethanol to clean the display by gently wiping. Moreover, when wiping the display, please wipe it by horizontal or vertical direction instead of circling to prevent leaving scars on the display's surface, especially polarizer.
- 6) Please wipe any unknown liquids immediately such as saliva, water or dew on the display to avoid color fading or any permanently damages.
- 7) Maximum pressure to the surface of the display must be less than 1.96×10^4 Pa. If the area of adding pressure is less than 1 cm^2 , the maximum pressure must be less than 1.96N.

12.3 PRECAUTIONS of OPERATING

- 1) Please input signals and voltages to the displays according to the values defined in the section of electrical characteristics to obtain the best performance. Any voltages over than absolute maximum rating will cause permanent damages to this display. Also, any timing of the signals out of this specification would cause unexpected performance.
- 2) When the display is operating at significant low temperature, the response time will be slower than it at 25 C°. In high temperature, the color will be slightly dark and blue compared to original pattern. However, these are temperature-related phenomenon of LCD and it will not cause permanent damages to the display when used within the operating temperature.
- 3) The use of screen saver or sleep mode is recommended when static images are likely for long periods of time. This is to avoid the possibility of image sticking.
- 4) Spike noise can cause malfunction of the circuit. The recommended limitation of spike noise is no bigger than ± 100 mV.

NO.

12.4 PRECAUTIONS of STORAGE

If the displays are going to be stored for years, please be aware the following notices.

- 1) Please store the displays in a dark room to avoid any damages from sunlight and other sources of UV light.
- 2) The recommended long-term storage temperature is between 10 C° ~35 C° and 55%~75% humidity to avoid causing bubbles between polarizer and LCD glasses, and polarizer peeling from LCD glasses.
- 3) It would be better to keep the displays in the container, which is shipped from KOE, and do not unpack it.
- 4) Please do not stick any labels on the display surface for a long time, especially on the polarizer.

12.5 PRECAUTIONS of IMAGE STICKING

- 1) Do not display the fixed image or very frequently repeated clips in a long period of time, it may cause image sticking on display. Even a video of several minutes, which is played in a loop, is considered as repetitive.
- Screensaver or power saving mode is recommended to avoid image sticking effectively. Using moving images, scrolling text and alternating a fixed image with a moving image, are the ideal ways to reduce the possibility of image sticking.
- 3) Additionally, it is important to avoid using static bars at image boundaries. Typically, such bars are a result of difference in aspect ratio (e.g., playing 4:3 content on a 16:9 display).

KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2712-TX18D200VM0EAA-1	PAGE	12-2/2
---------------------------------	--------------	------------------------------	------	--------

13. DESIGNATION of LOT MARK

1) The lot mark is showing in Fig.13.1. First 4 digits are used to represent production lot, T represented made in Taiwan, and the last 6 digits are the serial number.

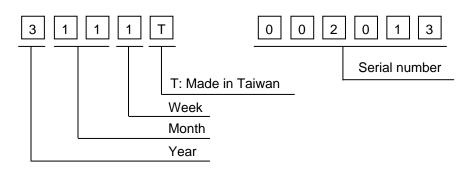


Fig.	13.1
3	-

2) The tables as below are showing what the first 4 digits of lot mark are shorted for.

Year	Lot Mark
2013	3
2014	4
2015	5
2016	6
2017	7

Month	Lot Mark	Month	Lot Mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

Week	Lot Mark		
1~7 days	1		
8~14 days	2		
15~21 days	3		
22~28 days	4		
29~31 days	5		

3) Except letters I and O, revision number will be shown on lot mark and following letters A to Z.

4) The location of the lot mark is on the back of the display shown in Fig. 13.2.



Fig. 13.2

KAOHSIUNG OPTO-ELECTRONICS INC.	SHEET NO.	7B64PS 2713-TX18D200VM0EAA-1	PAGE	13-1/1
---------------------------------	--------------	------------------------------	------	--------



Our company network supports you worldwide with offices in Germany, Austria, Switzerland, the UK and the USA. For more information please contact:

Headquarters

Fortec Group Members



Austria



DISTEC

DISTEC

A FORTEC GROUP MEMBER

A FORTEC GROUP MEMBER

FORTEC Elektronik AG Augsburger Str. 2b 82110 Germering

Phone: E-Mail: Internet: +49 89 894450-0 info@fortecag.de www.fortecag.de

Distec GmbH Office Vienna

Nuschinggasse 12 1230 Wien

Phone: E-Mail: Internet: +43 1 8673492-0 info@distec.de www.distec.de

Distec GmbH Augsburger Str. 2b 82110 Germering

Phone: E-Mail: Internet: +49 89 894363-0 info@distec.de www.distec.de

ALTRAC AG

Bahnhofstraße 3 5436 Würenlos

Phone: E-Mail: Internet: +41 44 7446111 <u>info@altrac.ch</u> <u>www.altrac.ch</u>

Display Technology Ltd.

Osprey House, 1 Osprey Court Hichingbrooke Business Park Huntingdon, Cambridgeshire, PE29 6FN

Phone: E-Mail: Internet: +44 1480 411600 info@displaytechnology.co.uk www. displaytechnology.co.uk

Apollo Display Technologies, Corp. 87 Raynor Avenue,

Unit 1Ronkonkoma, NY 11779

Phone: E-Mail: Internet: +1 631 5804360 info@apollodisplays.com www.apollodisplays.com



Germany



United Kingdom





A FORTEC GROUP MEMBER



